

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	141484	current adj source	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 13:27
L2	3253	375/374,376.ccls. 327/148,157.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 13:28
L3	714	1 and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 13:29
L4	2730228	switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 13:29
L5	612	3 and 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 13:30
L6	1249	327/536.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 13:38
L7	80048	pullup pull-up (pull\$2 adj up)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 13:39
L8	66348	pulldown pull-down (pull\$2 adj down)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 13:39
L9	20627	7 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 13:40

## EAST Search History

L10	4441	2 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 13:40
L11	62	1 and 4 and 9 and 10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 13:42

**Inventor Name Search Result**

Your Search was:

Last Name = JUNG

First Name = SOON-GIL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08187545</a>	<a href="#">5402180</a>	150	01/28/1994	RGB ENCODER FOR PRODUCING A COMPOSITE VIDEO SIGNAL FOR NTSC AND PAL SYSTEMS	JUNG, SOON-GIL
<a href="#">09504160</a>	<a href="#">6683740</a>	150	02/15/2000	ARCHITECTURE FOR A HARD DISK DRIVE WRITE AMPLIFIER CIRCUIT WITH DAMPING CONTROL	JUNG, SOON-GIL
<a href="#">09504970</a>	Not Issued	161	02/15/2000	Architecture for a hard disk drive write amplifier circuit with programmable controls	JUNG, SOON-GIL
<a href="#">10672798</a>	Not Issued	30	09/26/2003	Differential charge pump	JUNG, SOON-GIL
<a href="#">10738382</a>	<a href="#">7088082</a>	150	12/16/2003	REGULATOR WITH VARIABLE CAPACITOR FOR STABILITY COMPENSATION	JUNG, SOON-GIL
<a href="#">60160800</a>	Not Issued	159	10/21/1999	ARCHITECTURE FOR A HARD DISK DRIVE WRITE AMPLIFIER CIRCUIT WITH PROGRAMMABLE CONTROLS	JUNG, SOON-GIL
<a href="#">60160858</a>	Not Issued	159	10/21/1999	ARCHITECTURE FOR A HARD DISK DRIVE WRITE AMPLIFIER CIRCUIT WITH DAMPING CONTROL	JUNG, SOON-GIL

**Inventor Search Completed: No Records to Display.**

**Search Another: Inventor**
Last Name 
First Name

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)